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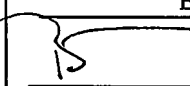
REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2113

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/767,850
Filed: January 29, 2004
Inventor(s):
Yaniv Vakrat and
Victor Rosenman

Title: PARALLEL TEXT
EXECUTION ON LOW-
END EMULATORS AND
DEVICES

§ Examiner: Wilson, Yolanda
§ Group/Art Unit: 2113
§ Atty. Dkt. No: 5681-62301
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****CERTIFICATE OF E-FILING TRANSMISSION****	
I hereby certify that this correspondence is being transmitted via electronic filing to the United States Patent and Trademark Office on the date shown below	
B. Noël Kivlin	
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	July 5, 2007
Signature	Date

RESPONSE TO FINAL OFFICE ACTION OF
APRIL 5, 2007

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Final Office Action of April 5, 2007, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.